

<b>Notice of References Cited</b>	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	09/654,115	KLEIN, DEAN A.	
	Examiner	Art Unit	Page 1 of 1
Tuan A Vu	2124		

**U.S. PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-6,205,545	03-2001	Shah et al.	712/237
B	US-5,721,893	02-1998	Holler et al.	712/239
C	US-6,115,809	09-2000	Mattson et al.	712/239
D	US-6,301,652	10-2001	Prosser et al.	712/204
E	US-6,249,911	06-2001	Arimilli et al.	717/151
F	US-5,704,053	12-1997	Santhanam, Vatsa	717/158
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

**FOREIGN PATENT DOCUMENTS**

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

**NON-PATENT DOCUMENTS**

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
U	McFarling, S., "Programming Optimization for instruction caches", 3rd International Conference on Architecture Support for Programming Languages and Operating Systems, vol. 24, pp. 183-191, April 1989.
V	
W	
X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.